Specifications

- **Power Source**: 9V Battery x 1 or AC adapter (BOSS PSA Series)
- **Current Draw**: 45mA or 60mA
- **Sampling Time**: 200ms (MIN) to 800ms (MAX)
- **Delay Time**: 50ms (MIN) to 800ms (MAX)
- **Frequency Response**: Sampling / Delay: 40Hz to 74Hz (±1/-3 dB)
- **Direct**: 1kHz to 60Hz (±1/-3 dB)
- **Residual Noise**: Sampling / Delay mode: ~95dBm (IHF-A)
  Normal mode: 100dBm or less (IHF-A)
- **Input Impedance**: 1MΩ (IHF input)
- **Output Load Impedance**: 10kΩ or more
- **Dimensions**: 200W x 55H x 125D (mm) / 2.31/4"(W) x 2.31/16"(H) x 4.15/16"(D)
- **Weight**: 435g / 1lb

*This notes includes the contents of the DSD-2 First Edition and makes it obsolete.
*The difference between DSD-2 and DSD-3 is nothing but the pedal.
*DSD-2のサービスノート第一版は廃版とし本サービスノートに併合します。
*DSD-2とDSD-3の違いはペダルだけです。他は全く同じです。

**Casing**
- **Case**: 2201018200 (DSD-2)
- **Pedal**: 2201018200 (DSD-3)
- **Panel**: 2202011600
- **Cover**: 2303050000
- **Pedal Mat**: 2267038600

**PCB**
- **Effect Board**: 75212550 (p/n 2291096001)
  Replacement: Effect Board includes Volume Board and SV Board.
- **Volume Board**: 75228530 (p/n 2291096001)
- **SV Board**: 75228540 (p/n 2291096001)

**IC**
- **IC-1**: B4231011 / B4231012
  C-HOS gate array
- **IC-2**: M27C250-15
  E²PROM 1500000
- **IC-3**: S570
  Comparer IR
- **IC-4**: TCM4350D
  D/A converter
- **IC-5**: B10100FR
  NAND
- **IC-6**: B10100FR
  Analog switch
- **IC-7**: M51BL
  OP amp
- **IC-8**: MJ21340
  Analog comp
- **IC-9**: MJ21310
  Flip-flop
- **IC-10**: U078003
  3-terminal voltage regulator

**Transistor**
- **Transistor**: 15121401
  2N3904
- **Transistor**: 15121417
  2N3904
- **Transistor**: 15121425
  2N3904

**Diode**
- **Diode**: 15121400
  1N4148
- **Diode**: 15121401
  1N4149
- **Diode**: 15121410
  1N4149

**Potentiometer**
- **Potentiometer**: 1329755
  R1210
- **Potentiometer**: 1329756
  R1210
- **Potentiometer**: 1329757
  R1210
- **Potentiometer**: 1329758
  R1210

**Jack**
- **Jack**: 13449140
  3P-1/4"-90°
- **Jack**: 13449140
  3P-1/4"-90°

**Switch**
- **Switch**: 11290131
  1N04D
- **Switch**: 11290130
  1N04A

**Resistor**
- **Resistor**: 13129133
  1N04A
- **Resistor**: 13129130
  1N04A

**Flattable**
- **Flattable**: 2347014000
  4P 180L
- **Flattable**: 2347014000
  3P 90L
- **Flattable**: 2347014000
  4P 180L
- **Flattable**: 2347014000
  3P 180L

**Miscellaneous**
- **Miscellaneous**: 2212010000
  Guide Bushing
- **Miscellaneous**: 2212010000
  Cushion
- **Miscellaneous**: 2212010000
  Coil Spring
- **Miscellaneous**: 2212010000
  Battery Snap
- **Miscellaneous**: 2212010000
  Shield Sheet
- **Miscellaneous**: 2212010000
  Plastic Sheath
WAVEFORMS

Boxed numbers correspond to those (check points) on the schematic diagram.

INPUT signal: 1kHz

1. 620mVpp
2. 400mVpp
3. 2.3Vpp S/Ned
4. 2.2Vpp
5. 200mVpp

Prior to SN573100 SN573100未満 (pcb 2292016800) DSD-2 ONLY

The circuit is changed due to factory requirements - no performance difference between this and modified one.

SN573100以後、点検後の部分が変更になっています。しかし性能上の違いはありません。

EFFECT BOARD
75228520 (pcb 2292016801)
SN573100--UP ... (DSD-2)

VOLUME BOARD
75228530 (pcb 2292016801)

EFFECT BOARD
75228520 (pcb 2292016801)

DC SUPPLY BOARD
75228550 (pcb 2291097800)

LED BOARD
(pcb 2291049600)

SW BOARD
75228540 (pcb 2292016801)

Replacement PCB is supplied in a set of three PCBs as shown below.

View from foil side
ADJUSTMENT

CLOCK FREQUENCY  VCO周波数レンジ設定

This adjustment is to set the range of Master Clock (MSCK) frequency at IC12 VCO.

1. High End

Adjust RT-1 on Effect Board for T = 12.12µs (82.5K ± 1kHz).

The MSCK should be 5.28MHz ± 64kHz.

この時マスターコックは5.28MHz±64kHzになる。

1. Low End

Adjust RT-2 on Volume Board for T = 50µs (20K ± 0.2 kHz).

The MSCK should be 1.28M ± 12.8kHz.

この時マスターコックは1.28MHz±12.8kHzになる。

IC DATA

M5K4164ANL-15

Pin Configuration (Top View)

Connect the scope to pin 13 of IC4 (or IC7 pin 18 SAHI).

オシロスコープをIC 4のピン13かIC 7のピン18に接続する。

M5218L

Pin Configuration

Adjust RT-2 on Volume Board for T = 50µs (20K ± 0.2 kHz).

The MSCK should be 1.28M ± 12.8kHz.

この時マスターコックは1.28MHz±12.8kHzになる。

NE570

Pin Configuration (Top View)

The MSCK should be 5.28MHz ± 64kHz.

この時マスターコックは5.28MHz±64kHzになる。

BA634

Pin Configuration

The MSCK should be 5.28MHz ± 64kHz.

この時マスターコックは5.28MHz±64kHzになる。

NJM311D

Pin Configuration (Top View)

The MSCK should be 5.28MHz ± 64kHz.

この時マスターコックは5.28MHz±64kHzになる。

311

Pin Configuration (Top View)