D-10

SERVICE NOTES
First Edition

SPECIFICATIONS/仕様

SPECIFICATIONS

KEYBOARD
61 key, 5 octave, C scale with Velocity

TUNE
MASTER TUNE
±50 cents
FINE TUNE
±50 cents

PITCH MODULATION
LFO
±177 cents
ENV
±5000 cents
VENDER
±2400 cents

ENV TIME
PITCH T1 - T4
4ms - 17s
TVF T1 - T4
4ms - 22s
TVA T1 - T4
4ms - 22s

LFO
RATE
4ms - 17s
OUTPUT
AUDIO +2dBm
PHONES +10dBm

POWER CONSUMPTION
20W, 15W (Japan)

DIMENSIONS
974(W) x 301(D) x 98(H) mm/38-3/8" x 11-7/8" x 3-7/8"

WEIGHT
8.8kg/19 lb 7 oz

INTERNAL MEMORY
Synthesizer Section
Patches 128
Timbers 128
Preset Tones 128
Programmable Tones 64
Preset Rhythm Tones 63
Rhythm Section
Setups 85 types (C1 to C8)
Rhythm Pattern
Preset Patterns 32
Programmable Patterns 32

Maximum number of notes
simultaneously recordable 8
Maximum number of notes
recordable (in each Rhythm Pattern) 96
Rhythm Track
Maximum number of bars recordable 500

MEMORY CARD (M-256D)

Power Switch WKA44 6A/250V (13149108)
Card Slot (22200188)

Jack YKB21-5010 (13449146)

AC CORD
(100V: 23495117)
(117V: 23495113)
(220V: 23495116)
(240V: 23495114)
(240V E: 23495115)

DISASSEMBLING/分解手順

1. Remove screws A.
2. Remove screws B.
EXPLODED/分解図

No. | PART NAME | PART No.
--- | --- | ---
1 | Front Panel | 22215628
2 | Side Panel R | 22215626
3 | Power Transformer Holder | 22025164
4 | Power Switch | 13149108
5 | Switch Cover | 22045414
6 | Bottom Cover | 22045617
7 | Fuse Board 100V | 7619715000
    | 117V | 7619715020
    | 220/240V | 7619715400
8 | Power Transformer (universal) | 22465512200
9 | Power Supply Board 100V | 7619712200
    | 107V | 7619712200
    | 220/240V | 7619712400
10 | LCD Cover | 22045412
11 | Volume Escatcheon | 22025329
12 | Volume Cover | 22045171
13 | LCD Holder | 2205161
14 | Panel Board | 7619708000
15 | LCD Dust Cover | 22045466
16 | LCD Unit | 15029465
17 | Side Holder L | 2205166
18 | Bender Unit | 2205670
19 | Bender Panel | 22025165
20 | Main Board | 7619705000
21 | Keyboard | 7619702000
22 | Card Slot Holder | 22001188
23 | Jack Holder | 2205162
PARTS LIST

Chip components except for special parts are excluded in this list.
Unlisted chip components such as capacitors and resistors are considered to be substituted by locally available ordinary ones.

SAFETY PRECAUTIONS:
The parts marked △ have safety-related characteristics.
Use only listed parts for replacement.

CASING
2221683 Front Panel
2311617 Bottom Cover
2311618 Back Plate
2311636 Speaker Cover
2211625 Header Panel
22364116 LED Panel
2226329 Volume Equaliser
2296414 LCD Cover
22245171 Volume Cover
22245468 LCD Dust Cover
1235638 Rubber Foot RS-09
1235644 Switch Cover

BUTTON, KNOB
2246592 Knob VOLUME, VALUE
2247565 Button single START, ROM PLAY, etc.
(with LED window)
2247567 Button dual SYNTH, A/B
(with LED window)
2247568 Button single STOP, TEMPO, INT/CARD, etc.
2249509 Button dual BANK 1, BANK 2
2249567 Button dual EXIT, EDIT, MOD, ENTER, etc.
(with LED window)
2249569 Button dual LOWER, UPPER
(with LED window)

HOLDER
1219670 **BI-1 battery holder retainer
1221688 ** MEC
2220116 Jack
2220138 Card slot
2220156 Slide L
2220184 Power transformer LCD
22205161

KEYBOARD
761878000 61 key See KEYBOARD PARTS LIST for details.

BENDER UNIT
232578 PB-A0101

LCD UNIT
1602946 LM162804 with LED, PCB and wiring

IC
(main board)
15179276 809BH CPU
15446829 P2D725615 - EP-ROM A
15446830 P2D725615 - EP-ROM B
15178979 HME3225P2Z00 ROM (reverb)
15178973 LRH3510 ROM (TONE)
15178978 HME3258SRP08 ROM (PCM)
15178980 HME3258SRP010 ROM (PCM B)
15228989 M61138050-00001 synth chip
15229663 HG12122SF6F42F reverb chip
1523106 HG04112SF-72F gate array
15229568 P5005905G-062 gate array
15259330 M9S91419FP G-BGD gate array
15779056 SRM264MS-16 64K SRAM
15779051 HME256FLP-16 256K SRAM
15275069 LC317AM-12 1K SRAM
15275012 HM1118
15179380 P2D41419C-12 DRAM
15219192 PCM54 D/A converter
15209105 PC4570G low noise OP AMP (dual in line)
15209106 M5282FF low noise OP AMP (dual in line)
15182101 BA1219P FET OP AMP (dual in line)
15289110 PC4502G FET OP AMP (dual in line)
15260100 SMCNL40NS 16P TAP
152601070 TC4203CF-72 quad 2 input NAND gate
152601070 TC4203CF-72 quad 2 input NAND gate
152601070 TC4203CD-72 dual 4 input AND gate
152601070 TC4204CH-72 triple 3 input OR gate
1526082070 TC4204CM-72 8-channel analog/multiplexer
15191972 T457810PEP 5V voltage regulator
15289113 TMS250F1P-0723F regulator array
15289114 TMS250F1P-0723F regulator array
15289115 TMS250F1P-0723F power supply socket
151519105 L991815 5V voltage regulator
151519115 L991812 12V voltage regulator
151519107 L991812 12V voltage regulator

OPT-ISOLATOR
1522970050 PC-910 main board

FUSE
△1255868 1098 fuse board (100V)
△1255868 fuse board (117V)
△1255868 fuse board (220V)
△1255868 fuse board (240V)
△1255868 fuse board (240V)

BATTERY
1269249 CR2025 lithium

PVC/COAD
△22469117 100V
△22469113 117V
△2246916 220V
△22469114 240V Australia
△22469115 240V England

PCB ASSEMBLY
761790500 Main Board (PCB 2295262)
761790500 Panel Board (PCB 2295263)
761791200 Power Supply Board 100V (PCB 2295524)
761791200 Power Supply Board 117V (PCB 2295526)
761791200 Power Supply Board 220V (PCB 2295544)
761791200 Power Supply Board 230V (PCB 2295546)
761791520 Fuse Board 117V (PCB 2295524)
761791520 Fuse Board 220V (PCB 2295564)

Power Supply Board 220V (PCB 2295564)

POWER TRANSFORMER
△224691120 Universal 100/117/220/240V

No replacement for individual parts.

No replacement for individual parts.

No replacement for individual parts.
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<thead>
<tr>
<th>No.</th>
<th>PARTS No.</th>
<th>PARTS NAME</th>
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<tbody>
<tr>
<td>1</td>
<td>2557510</td>
<td>NATURAL KEY C-F</td>
</tr>
<tr>
<td>2</td>
<td>2557513</td>
<td>NATURAL KEY D</td>
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<td>3</td>
<td>2557515</td>
<td>NATURAL KEY E-B</td>
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<td>2557516</td>
<td>NATURAL KEY G</td>
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<td>NATURAL KEY C-F</td>
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<td>22895451</td>
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<td>22155716</td>
<td>HP-GUIDE RUSH</td>
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<td>11</td>
<td>22189210</td>
<td>CONTACT RUBBER 12P</td>
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<td>22189219</td>
<td>CONTACT RUBBER 13P</td>
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<td>13</td>
<td>22445144</td>
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<td>15</td>
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<td>P.C. B 2SP ASSY</td>
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<td>16</td>
<td>22135415</td>
<td>SK-3 STOPPER A</td>
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<td>17</td>
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<td>SK-3 STOPPER B</td>
</tr>
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<td>SK-3 STOPPER C</td>
</tr>
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<td>SK-3 ANGLE A-JX</td>
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<td>22</td>
<td>22175187</td>
<td>SK-3 SPRING (NATURAL)</td>
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<td>23</td>
<td>22175188</td>
<td>SK-3 SPRING (SHARP)</td>
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<td>24</td>
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<td>NYLON RIVET NRB-345</td>
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<tr>
<td>25</td>
<td></td>
<td>TAPPING SCREWS 3X8X BT</td>
</tr>
</tbody>
</table>

**DIODE**

- (main board) 15399104 RLS-71 TE-11 chip
- 15399105 DAN202K T-96 chip
- 15399103 MA-153 chip

**CAPACITOR**

- (main board) 153941050 ECEAEIEN1005BP 10 uF±15%BP
- 1539894650 25MVHR70A+T 47 uF±15%V
- 1539984650 16MVHR70A+T 2.7 uF±15%V
- 1539894950 5MV8R100BP 100 uF±15%V
- 1539989550 5MV4R100A+T 47 uF±15%V
- 1539989520 5MVR4100A+T 100 uF±15%V
- 1539899520 4MV100A+T 100 uF±15%V
- 1539410920 ECG-MHI472JF3 0.015 uF
- 1539410990 ECG-MHI5152JF3 0.0015 uF

**CARRIER ARRAY**

- 1539219310 N320220K 22P R. X8 320220K 22P R. X8
- 1539219410 CX22020K 100P X 8

**RESISTOR ARRAY**

- 15399921045 MWDRLSX231135K 155 X 4
- 15399921034 MWDRLSX222252X 2.2X 4
- 15399921017 MWDRLSX222252X 2.2X 4
- 15399921008 MWDRLSX280108X 100 X 4

**POTENTIOMETER**

- 13399217 RYVRP15-15.01N-100K 100K

**SWITCH**

- (main board) 1531449103 WCAAA1.6A 250V power
- 1531449104 SOA-1235B panel board

**SOCKET**

- 1342954120 260-7234-51-3951 CPU
- 1342954121 100-220-800 EPROM
- 1342954130 YK8C-2010L(mono) PHONES
- 1342954140 YK8C-2010L(mono) OUTPUT, CONTROL INPUT
- 1342954150 MID3-N3(triplet) MID IN/OUT/THRU

**CONNECTOR**

- (w/ trap) 13429411 S2004-0610 6P main board CN2
- 13429412 S2004-0710 7P main board CN12
- 13429413 S2004-1210 12P main board CN4
- 13429425 S2004-1310 13P main board CN5
- 13429436 S2004-1410 14P main board CN6
- (collar holder) 13429463 SD-6106-0600 6P power supply board CN2
- 13429467 SD-6106-0700 7P power supply board CN1
- 13429468 SD-6106-0800 8P main board CNE, CN6, CN10
- 13429422 SD-6106-1210 12P panel board CN3
- (pin header) 13429330 IL-S-3P-S272-EF 6P main board CN7
- 13429335 IL-S-4P-S272-EF 6P main board CN13
- 13429358 IL-S-10P-S272-EF 10P main board CN11
- (memory card connector) 13429353 750085A main board CN3

**FILTER**

- 12460232 SST-0450 main board
- 13521945 NV3010-65Z72A550 main board
- 13521954 DSS306-55F2322T6 main board

**INDUCTOR**

- 12460232 BLDR202-R25 main board
- 12460234 BLDR202-R25 main board

**MISCELLANEOUS**

- 23450316 Grounding Leaf
TEST MODE

Leave all sockets and card slot disengaged except for AC inlet.

Press and hold EXIT and EDIT buttons and then switch the power on.

EXIT + EDIT Memory test
EXIT + TUNE A/D test, switch test
EXIT + MIDI Keyboard test
EXIT + COMPARE D/A Adjust, PCM wave test
EXIT + WRITE All LCD dots cleared
EXIT + DATA TRANSFER All LCD dots light
EXIT + ENTER LED test

During the test mode the following buttons serve as test selector button.

| EXIT + EDIT | Memory test |
| EXIT + TUNE | A/D test, switch test |
| EXIT + MIDI | Keyboard test |
| EXIT + COMPARE | D/A Adjust, PCM wave test |
| EXIT + WRITE | All LCD dots cleared |
| EXIT + DATA TRANSFER | All LCD dots light |
| EXIT + ENTER | LED test |

MEMORY TEST

Holding down EXIT, press EDIT.
EXIT を押ししながら EDIT を押す。

Memory Test
W=—— T=—— C=——

Working RAM
STOP displayed?
"W=OK" と表示する。
YES

CHECK: Main board IC10, 11

With memory protect up on, Holding EXIT, press EDIT.
メモリーカード(RAM) のプロテクトスイッチを ON にして全体に挿入し、EXIT を押しながら EDIT を押す。

T=OK displayed?
"T=OK" と表示する。
YES

CHECK: Main board IC9

Set the memory protect to off, Holding EXIT, press EDIT.
メモリープロテクトスイッチを OFF にして EXIT を押しながら EDIT を押す。

C=OK displayed?
"C=OK" と表示する。
YES

Memory Card test is complete.

Description on C messages in memory test.
メモリーテストでは以下の表示によって次のチェックが正しく行われた事を示す。

C = NC Card not ready
C = P. ON Memory card protect on
C = OK Memory card read/write OK

Memory Card test is complete.
RECOVERING TONE RAM DATA

Recovering TONE RAM Data

Press the "LOAD" button on the left side of the machine to load the recovered data into the machine's memory.

DATA TRANSFER

Press the "DATA TRANSFER" button to begin the data transfer process.

Complete

When the data has been transferred properly, the display shows "Complete," then returns to the Play mode.

Memory Protection is automatically restored to ON.
To disconnect the cable —

1. Pull out the two tabs on the side of the connector.

2. Pull the cable free from the connector.

3. Lead the cable through the filter to the position indicated.

4. Lead the cable through the filter to the position indicated.

5. Lead the cable through the filter to the position indicated.

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97. Lead the cable through the filter to the position indicated.

98. Lead the cable through the filter to the position indicated.

99. Lead the cable through the filter to the position indicated.

100. Lead the cable through the filter to the position indicated.
### Linear Synthesizer (Performance Mode)

**Model D-10**

#### MIDI Implementation Chart

<table>
<thead>
<tr>
<th>Function</th>
<th>Transmitted</th>
<th>Recognized</th>
<th>Remarks</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Basic Channel</strong></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Default</td>
<td>1-16</td>
<td>1-16</td>
<td>Memorized</td>
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<tr>
<td>Changed</td>
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<td><strong>Mode</strong></td>
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<tr>
<td>Default</td>
<td>Mode 3</td>
<td>Mode 3</td>
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<td>Messages</td>
<td>POLY, OMNI OFF</td>
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<td><strong>Note Number</strong></td>
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<td>True voice</td>
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<td>0-127</td>
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<td><strong>Velocity</strong></td>
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<tr>
<td>Note ON</td>
<td>o v= 1-127</td>
<td>o v= 1-127</td>
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<td>Note OFF</td>
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<td><strong>After Touch</strong></td>
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<td>Key's</td>
<td>x</td>
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<td>Ch's</td>
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<td><strong>Pitch Bender</strong></td>
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<tr>
<td>*</td>
<td>* 0-24 semi</td>
<td>9 bit resolution</td>
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</table>

#### Control Change

| 64                | 100, 101    | 121        |
| ****              |            | *** (O)    |
| Hold 1             | RPC LSB, MSB |           |
| ** Reset all controllers** |

#### Prog Change

| * 0-127           | * 0-127     |
| ** Exclusive**    |            |
| ** System**       |            |
| Song Pos          | x           |
| Song Sel          | x           |
| Tune              | x           |
| ** Common**       |            |
| ** System Real Time** |
| Clock             | x           |
| Commands          | x           |
| ** Aux Messages** |            |
| Local ON/OFF      | x           |
| All Notes OFF     | (123-127)   |
| ** Notes**        | * Can be set to ○ or × manually, and memorized. |
|                   | ** Can be set to ○ or × manually. |
|                   | *** RPC = Registered parameter control number. |
|                   | RPC ≠ 0 : Pitch bend sensitivity. |
|                   | Parameter values are given by Data Entry. |

**Mode 1**: OMNI ON, POLY  
**Mode 2**: OMNI ON, MONO  
**Mode 3**: OMNI OFF, POLY  
**Mode 4**: OMNI OFF, MONO

### Linear Synthesizer (Multi Timbral Mode/Synthesizer Section)

**Model D-10**

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<tr>
<td>Default</td>
<td>x</td>
<td>1-16</td>
<td>Memorized</td>
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<td>Changed</td>
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<tr>
<td><strong>Mode</strong></td>
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<td>Default</td>
<td>Mode 3</td>
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<td>Messages</td>
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</tr>
<tr>
<td><strong>Note Number</strong></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>True voice</td>
<td>x</td>
<td>0-127</td>
<td></td>
</tr>
<tr>
<td><strong>Velocity</strong></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Note ON</td>
<td>x</td>
<td>x</td>
<td></td>
</tr>
<tr>
<td>Note OFF</td>
<td></td>
<td>x</td>
<td></td>
</tr>
<tr>
<td><strong>After Touch</strong></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Key's</td>
<td>x</td>
<td>x</td>
<td></td>
</tr>
<tr>
<td>Ch's</td>
<td>x</td>
<td>x</td>
<td></td>
</tr>
<tr>
<td><strong>Pitch Bender</strong></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>*</td>
<td>○ 0-24 semi</td>
<td>9 bit resolution</td>
<td></td>
</tr>
</tbody>
</table>

#### Control Change

| 64                | 100, 101    | 121        |
| ** Hold 1 RPC LSB, MSB** |
| ** Reset all controllers** |

#### Prog Change

| * 0-127           | * 0-127     |
| ** Exclusive**    |            |
| ** System**       |            |
| Song Pos          | x           |
| Song Sel          | x           |
| Tune              | x           |
| ** Common**       |            |
| ** System Real Time** |
| Clock             | x           |
| Commands          | x           |
| ** Aux Messages** |            |
| Local ON/OFF      | x           |
| All Notes OFF     | (123-127)   |
| ** Notes**        | * Can be set to ○ or × manually. |
|                   | ** RPC = Registered parameter control number. |
|                   | RPC ≠ 0 : Pitch bend sensitivity. |
|                   | Parameter values are given by Data Entry. |

**Mode 1**: OMNI ON, POLY  
**Mode 2**: OMNI ON, MONO  
**Mode 3**: OMNI OFF, POLY  
**Mode 4**: OMNI OFF, MONO

* ○ : YES  
* × : NO
## LINEAR SYNTHESIZER (Multi Timbral mode/Keyboard section)

**MIDI Implementation Chart**

**Model D-10**

<table>
<thead>
<tr>
<th>Function</th>
<th>Transmitted</th>
<th>Recognized</th>
<th>Remarks</th>
</tr>
</thead>
<tbody>
<tr>
<td>Basic Channel</td>
<td>Default</td>
<td>1-16</td>
<td>×</td>
</tr>
<tr>
<td>Mode</td>
<td>Default Messages Altered</td>
<td>Mode 3 POLY, OMNI OFF</td>
<td>×</td>
</tr>
<tr>
<td>Note Number</td>
<td>True voice</td>
<td>24-108</td>
<td>×</td>
</tr>
<tr>
<td>Velocity Note ON Note OFF</td>
<td>v = 1-127 9n v = 0</td>
<td>×</td>
<td></td>
</tr>
<tr>
<td>After Touch Key’s Ch’s</td>
<td>×</td>
<td>×</td>
<td></td>
</tr>
<tr>
<td>Pitch Bender **</td>
<td>×</td>
<td>Modulation</td>
<td></td>
</tr>
<tr>
<td>Control Change</td>
<td>Prog Change</td>
<td>Ture ≠</td>
<td>0-127</td>
</tr>
<tr>
<td>System Exclusive</td>
<td>×</td>
<td>×</td>
<td></td>
</tr>
<tr>
<td>System Song Pos Song Sel Tune</td>
<td>×</td>
<td>×</td>
<td></td>
</tr>
<tr>
<td>System Real Time Clock Commands</td>
<td>×</td>
<td>×</td>
<td></td>
</tr>
<tr>
<td>Aux Local ON/OFF All Notes OFF Active Sense Reset</td>
<td>×</td>
<td>(123)</td>
<td>×</td>
</tr>
<tr>
<td>Notes</td>
<td>×</td>
<td>Can be set to ○ or × manually.</td>
<td></td>
</tr>
</tbody>
</table>

Mode 1: OMNI ON, POLY  
Mode 2: OMNI ON, MONO  
Mode 3: OMNI OFF, POLY  
Mode 4: OMNI OFF, MONO  

---

## LINEAR SYNTHESIZER (Rhythm section)

**MIDI Implementation Chart**

**Model D-10**

<table>
<thead>
<tr>
<th>Function</th>
<th>Transmitted</th>
<th>Recognized</th>
<th>Remarks</th>
</tr>
</thead>
<tbody>
<tr>
<td>Basic Channel</td>
<td>Default</td>
<td>1-16</td>
<td>1-16</td>
</tr>
<tr>
<td>Mode</td>
<td>Default Messages Altered</td>
<td>Mode 3</td>
<td>×</td>
</tr>
<tr>
<td>Note Number</td>
<td>True voice</td>
<td>24-108</td>
<td>24-108</td>
</tr>
<tr>
<td>Velocity Note ON Note OFF</td>
<td>v = 1-127 9n v = 0</td>
<td>×</td>
<td></td>
</tr>
<tr>
<td>After Touch Key’s Ch’s</td>
<td>×</td>
<td>×</td>
<td></td>
</tr>
<tr>
<td>Pitch Bender ×</td>
<td>×</td>
<td>0-24 semi</td>
<td>9 bit resolution</td>
</tr>
<tr>
<td>Control Change</td>
<td>Prog Change</td>
<td>Ture ≠</td>
<td>6 7 11</td>
</tr>
<tr>
<td>System Exclusive</td>
<td>○</td>
<td>×</td>
<td>Setup &amp; Seq data</td>
</tr>
<tr>
<td>System Song Pos Song Sel Tune</td>
<td>×</td>
<td>×</td>
<td></td>
</tr>
<tr>
<td>System Real Time Clock Commands</td>
<td>○ (Clock mode = INT) ○ (Clock mode = MID)</td>
<td>○ (Clock mode = INT) ○ (Clock mode = MID)</td>
<td></td>
</tr>
<tr>
<td>Aux Local ON/OFF All Notes OFF Active Sense Reset</td>
<td>×</td>
<td>×</td>
<td></td>
</tr>
<tr>
<td>Notes</td>
<td>×</td>
<td>Performance mode - Can be set to ○ or × manually and memorized. Multi Timbral mode - Always received. ** RPC = Registered parameter control number. RPC ≠ 0: Pitch bend sensitivity + + Can be set to ○ or × manually.</td>
<td></td>
</tr>
</tbody>
</table>

Mode 1: OMNI ON, POLY  
Mode 2: OMNI ON, MONO  
Mode 3: OMNI OFF, POLY  
Mode 4: OMNI OFF, MONO
LINEAR SYNTHESIZER

Model D-10

MIDI Implementation

Date: Dec. 21 1987
Version: 1.00

Status Second Third

Hold D-10

+ n = MIDI Channel

Note on

Transmitted only when in Internal clock mode.
Panel operation: Press Start button.

Note off

Transmitted only when in Internal clock mode.
Panel operation: Press Stop button.

Status

Hold

+ n = MIDI Channel

Note on

Transmitted only when in Internal clock mode.
Panel operation: Press Stop button.

Notes

Hold

+ n = MIDI Channel

Note off

Transmitted only when in Internal clock mode.
Panel operation: Press Stop button.

Hold-1

Status Second Third

Hold D-10

+ n = MIDI Channel

Note on

Transmitted only when in Internal clock mode.
Panel operation: Press Start button.

Note off

Transmitted only when in Internal clock mode.
Panel operation: Press Stop button.

Status

Hold

+ n = MIDI Channel

Hold-2

Status Second Third

Hold D-10

+ n = MIDI Channel

Note on

Transmitted only when in Internal clock mode.
Panel operation: Press Start button.

Note off

Transmitted only when in Internal clock mode.
Panel operation: Press Stop button.

Status

Hold

+ n = MIDI Channel

Hold-3

Status Second Third

Hold D-10

+ n = MIDI Channel

Note on

Transmitted only when in Internal clock mode.
Panel operation: Press Start button.

Note off

Transmitted only when in Internal clock mode.
Panel operation: Press Stop button.

Status

Hold

+ n = MIDI Channel

Hold-4

Status Second Third

Hold D-10

+ n = MIDI Channel

Note on

Transmitted only when in Internal clock mode.
Panel operation: Press Start button.

Note off

Transmitted only when in Internal clock mode.
Panel operation: Press Stop button.

Status

Hold

+ n = MIDI Channel

Hold-5

Status Second Third

Hold D-10

+ n = MIDI Channel

Note on

Transmitted only when in Internal clock mode.
Panel operation: Press Start button.

Note off

Transmitted only when in Internal clock mode.
Panel operation: Press Stop button.

Status

Hold

+ n = MIDI Channel

Hold-6

Status Second Third

Hold D-10

+ n = MIDI Channel

Note on

Transmitted only when in Internal clock mode.
Panel operation: Press Start button.

Note off

Transmitted only when in Internal clock mode.
Panel operation: Press Stop button.

Status

Hold

+ n = MIDI Channel

Hold-7

Status Second Third

Hold D-10

+ n = MIDI Channel

Note on

Transmitted only when in Internal clock mode.
Panel operation: Press Start button.

Note off

Transmitted only when in Internal clock mode.
Panel operation: Press Stop button.

Status

Hold

+ n = MIDI Channel

Hold-8

Status Second Third

Hold D-10

+ n = MIDI Channel

Note on

Transmitted only when in Internal clock mode.
Panel operation: Press Start button.

Note off

Transmitted only when in Internal clock mode.
Panel operation: Press Stop button.

Status

Hold

+ n = MIDI Channel

Hold-9

Status Second Third

Hold D-10

+ n = MIDI Channel

Note on

Transmitted only when in Internal clock mode.
Panel operation: Press Start button.

Note off

Transmitted only when in Internal clock mode.
Panel operation: Press Stop button.

Status

Hold

+ n = MIDI Channel

Hold-10

Status Second Third

Hold D-10

+ n = MIDI Channel

Note on

Transmitted only when in Internal clock mode.
Panel operation: Press Start button.

Note off

Transmitted only when in Internal clock mode.
Panel operation: Press Stop button.

Status

Hold
**DMLS OFF**

Status: Second Third

Raw: 7EH

- **M.S. Channel**: 84 - FE (1 - 16)
- **Recognized at All MA's**: Only

The D-10 stage in DMS 2.

**DMLS ON**

Status: Second Third

Raw: 7EC

- **M.S. Channel**: 84 - F8 (1 - 16)
- **Recognized on All MA's**: Only

The D-10 stage in DMS 2.

**M.N.O.**

Status: Second Third

Raw: 7F0

- **M.S. Channel**: 84 - FB (1 - 16)
- **Recognized on All MA's**: Only

The D-10 stage in DMS 2.

**POLY**

Status: Second Third

Raw: 7F4

- **M.S. Channel**: 84 - FA (1 - 16)
- **Recognized on All MA's**: Only

The D-10 stage in DMS 2.

**Exclusive**

Status

- **DR**: Active
- **DR**: Active, **DR**: Active

Data received is 9-bit binary format, each bit represents a status or data in sequence, at least within 100 ms intervals. If the bit fails to receive a data, the receiver waits for the next bit, and if all data is received, a complete message is terminated and the receiver is released. Note: All data is received and stored before initialization testing begins.

**Exclusive**

Status

- **DR**: Active

Data received is 9-bit binary format, each bit represents a status or data in sequence, at least within 100 ms intervals. If the bit fails to receive a data, the receiver waits for the next bit, and if all data is received, a complete message is terminated and the receiver is released. Note: All data is received and stored before initialization testing begins.

**B3**

Status

- **DR**: Active

Data received is 9-bit binary format, each bit represents a status or data in sequence, at least within 100 ms intervals. If the bit fails to receive a data, the receiver waits for the next bit, and if all data is received, a complete message is terminated and the receiver is released. Note: All data is received and stored before initialization testing begins.

**MONROM)**

Status: Second Third

Raw: 7F8

- **AL**: Active

Data received is 9-bit binary format, each bit represents a status or data in sequence, at least within 100 ms intervals. If the bit fails to receive a data, the receiver waits for the next bit, and if all data is received, a complete message is terminated and the receiver is released. Note: All data is received and stored before initialization testing begins.

**Initializing**

Status: Second Third

Raw: 7FC

- **AL**: Active

Data received is 9-bit binary format, each bit represents a status or data in sequence, at least within 100 ms intervals. If the bit fails to receive a data, the receiver waits for the next bit, and if all data is received, a complete message is terminated and the receiver is released. Note: All data is received and stored before initialization testing begins.

**Booting**

Status: Second Third

Raw: 80H

- **AL**: Active

Data received is 9-bit binary format, each bit represents a status or data in sequence, at least within 100 ms intervals. If the bit fails to receive a data, the receiver waits for the next bit, and if all data is received, a complete message is terminated and the receiver is released. Note: All data is received and stored before initialization testing begins.
<table>
<thead>
<tr>
<th>Column 1</th>
<th>Column 2</th>
<th>Column 3</th>
<th>Column 4</th>
<th>Column 5</th>
</tr>
</thead>
<tbody>
<tr>
<td>Value 1</td>
<td>Value 2</td>
<td>Value 3</td>
<td>Value 4</td>
<td>Value 5</td>
</tr>
<tr>
<td>Value 6</td>
<td>Value 7</td>
<td>Value 8</td>
<td>Value 9</td>
<td>Value 10</td>
</tr>
<tr>
<td>Value 11</td>
<td>Value 12</td>
<td>Value 13</td>
<td>Value 14</td>
<td>Value 15</td>
</tr>
<tr>
<td>Value 16</td>
<td>Value 17</td>
<td>Value 18</td>
<td>Value 19</td>
<td>Value 20</td>
</tr>
</tbody>
</table>

Note: The table continues with more rows and columns as per the document.
<table>
<thead>
<tr>
<th>Page</th>
<th>WRONG 誤</th>
<th>CORRECT 正</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>PART NUMBER ERROR Button dual (224755555)</td>
<td>Button dual (22495209)</td>
</tr>
</tbody>
</table>

* Please amend all existing service notes as above.
* 該当サービスノートを上記のように修正して下さい。
<table>
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<td>1</td>
<td>PART NUMBER ERROR</td>
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(D-20)

(D-10)

* Please amend all existing service notes as above.
* 該当キーのスノートを上記のように修正して下さい。